

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 53080
Hidenori KATSUMURA, et al. : Confirmation Number: 8158
Application No.: 10/591,255 : Group Art Unit: 2832
Filed: August 31, 2006 : Examiner: BAISA, JOSELITO SASIS
For: COMPONENT WITH COUNTERMEASURE TO STATIC ELECTRICITY

SUBSTANCE OF INTERVIEW SUMMARY

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

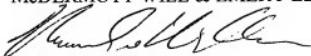
Sir:

Applicants appreciate the granting of the Examiner interview which took place on October 5, 2010. Applicants' representative, Mr. Nathaniel McQueen, agrees with the substance of the interview that the submitted amendment to claim 1 was discussed and explained that Wada does not teach a bismuth oxide diffusion layer that is in between the laminated varistor layer and the board, as is recited in the Interview Summary attached to the Notice of Allowance.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



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